

Notice of Allowability

Application No.

10/604,277

Examiner

Binh C. Tat

Applicant(s)

BEDNAR ET AL.

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment on 06/06/06.
2. ☒ The allowed claim(s) is/are 21-23, 25, 27-33, 35 and 37-48.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.


THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date 08/18/06.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.


PAUL DINH
PRIMARY EXAMINER

DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Anthony M. Palagonia (Reg. No. 41237) on 08/18/06.

The application has been amended as follow:

Claim 21 line 7, after " island V2" insertion --; wherein each voltage island of the N voltage islands includes an externally supplied VDDN power supply and a voltage shifting means, or said externally supplied VDDN power supply and a fencing means, or said externally supplied VDDN power supply, said voltage shifting means and said fencing means; and wherein each voltage island of the N voltage islands further includes one or more substructures selected from the group consisting of an internal voltage island VDDI power distribution network, state saving means, one or more switching elements coupled between said externally supplied VDDN power supply and said internal voltage island VDDI power distribution network, and one or more voltage buffering circuits--.

Claim 24 has been canceled.

Claim 25 line 1, change "claim 24" to --claim 21--.

Claim 26 has been canceled.

Claim 27 line 1, change "claim 26" to "claim 21".

Art Unit: 2825

Claim 28 line 1, change "claim 26" to "claim 21".

Claim 31 line 7, after " island V2" insertion --; wherein each voltage island of the N voltage islands includes an externally supplied VDDN power supply and a voltage shifting means, or said externally supplied VDDN power supply and a fencing means, or said externally supplied VDDN power supply, said voltage shifting means and said fencing mean; and wherein each voltage island of the N voltage islands further includes one or more substructures selected from the group consisting of an internal voltage island VDDI power distribution network, state saving means, one or more switching elements coupled between said externally supplied VDDN power supply and said internal voltage island VDDI power distribution network, and one or more voltage buffering circuits--.

Claim 34 has been canceled.

Claim 35 line 1, change "claim 34" to --claim 31--.

Claim 36 has been canceled.

Claim 37 line 1, change "claim 36" to "claim 31".

Claim 38 line 1, change "claim 36" to "claim 31".

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

Claims 21-23, 25, 27-33, 35, and 37-48 are allowed because the prior art does not teach or suggest an electrical structure/method having combinations of step/elements in the claims including particularly the following limitations in claim 21 and similarly recited claim 31: a logic circuit voltage power supply connected to a logic circuit and an externally supplied state saving power supply connected to a logic state saving circuit, said logic circuit and said state saving

Art Unit: 2825

circuit within either said voltage island V_1 or said voltage island V_2 ; wherein each voltage island of the N voltage islands includes an externally supplied VDDN power supply and a voltage shifting means, or said externally supplied VDDN power supply and a fencing means, or said externally supplied VDDN power supply, said voltage shifting means and said fencing means; and wherein each voltage island of the N voltage islands further includes one or more substructures selected from the group consisting of an internal voltage island VDDI power distribution network, state saving means, one or more switching elements coupled between said externally supplied VDDN power supply and said internal voltage island VDDI power distribution network, and one or more voltage buffering circuits.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Art Unit: 2825

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Binh C. Tat whose telephone number is 571 272-1908. The examiner can normally be reached on 7:30 - 4:00 (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571 272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tat Binh

Patent Examiner

August 16, 2006

PAUL DINH
PRIMARY EXAMINER

A handwritten signature in black ink, appearing to read "Paul Dinh", with a stylized flourish at the end.